



# WAVECREST CR Option Specifications

## Continuous Rate 30 Mb/s to 3.0 Gb/s Clock Recovery Option

The SIA-3000, with the addition of the clock recovery option, enables signal integrity compliance and diagnostics measurements when access to a bit clock is not available. The clock recovery option removes the need for awkward setups for obtaining accurate and repeatable compliant signal integrity measurements - simply plug in your test signal to make eye diagram and jitter measurements on any type of data signal, random or repeating.

### Specifications

Clock recovery data rate.....	30 Mb/s to 3 Gb/s
Clock recovery PLL bandwidth.....	Databaud/1244 Typical
Clock jitter <sup>1</sup>	
Databaud < 500 Mb/s .....	< 5.0ps RMS
Databaud > 500Mb/s .....	< 3.0ps RMS
Insertion Loss Through Path .....	8 dB Typical
Operating Input Signal Level	
Single Ended: .....	0.250 to 2.5 Volts(pk-pk)
Differential: .....	0.125 to 1.25 Volts(pk-pk)

<sup>1</sup>Measured using PRBS 2<sup>23</sup>-1 test pattern.

### Setup

The data from the DUT is sent into the Clock recovery Data In. The supplied Hard-line SMA's are used to connect from the Clock Recovery Data Out to the IN1 (Channel 1 input). The recovered clock is used internally for all signal integrity measurements.



### Applications

Fibre Channel (1x, 2x, 3x), SAS & SATA (1.5 & 3.0), Gigabit Ethernet, PCI Express, Infiniband, SONET OC-3,12,48, XAUI, 10GFC, Serial RapidIO, DVI, 1394b (800Mb/s & 1.6Gb/s), Fast Ethernet, HDMI, Custom (30 to 3000 Mb/s).

